

REMARKS

Claims 3 and 9 have been cancelled without prejudice and new claims 10-21 have been added. Claim 5 has been amended to correct the form by inserting "and" before the last element of the claim. Claim 7 has been amended to correct the form by inserting "a" before the words "voltage converter." Claim 6 has been amended for clarification purposes and to more distinctly claim and point out the invention. Support for amended claim 6 can be found in the original claim 6 as well as the in the specification at pages 10-11 and 14 as well as Figures 3 and 5. Support for new claims 10 and 21 can be found throughout the specification, for instance pages 10-13 and Figure 3. Support for new claim 11 can be found in the specification at pages 13-14 and Figure 4. Support for new claim 12 can be found in original claim 4 as well as throughout the specification, for instance pages 10-11 and 15. Support for new claims 13 and 19 can be found in the specification at pages 10-11 and 14 as well as Figures 3 and 5. Support for new claim 14 can be found in the specification, for instance, at pages 10 -13 and Figures 2 and 3. Support for new claims 15-18 and 20 can be found in the specification at pages 12 and 15-17 as well as Figures 3 and 6.

A check for \$354.00 to cover the fee for one additional claim over twenty and four additional independent claims is enclosed. It is not believed that any additional fees are due, however, should there be any additional fees, please charge the same to Deposit Account No. 02-4467.

In view of the foregoing, favorable action on the merits including entry and approval of all amendments and allowance of all claims is solicited.

Respectfully submitted,

By: Diane J. Diasparra
Diana J. Diasparra
Reg. No. 40,990
BRYAN CAVE LLP
245 Park Avenue
New York, New York 10167-0034
(212) 692-1800

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on June 20, 2002
(Date of Deposit)
Diana J. Diasparra
Name of applicant, assignee, or Registered Representative
<u>Diane Diasparra</u>
Signature
6/20/02
Date of Signature

EXHIBIT 1

"MARKED UP" AMENDMENT TO CLAIM PURSUANT TO RULE 1.121(c)

5. A video switch for switching a video output of one of a plurality of computers to a target video destination, the video switch comprising:

a first logic gate having a video control input, an OSD control input, and a control output, the control output of the first logic gate having a first voltage level;

a resistor divider network operably coupled to the control output of the first logic gate;

a second logic gate operably coupled to the resistor divider network, the second logic gate having a control output, the control output of the second logic gate having a second voltage level; and

a discrete radio frequency switch having a control, a video input and a video output, wherein the video output of one of the plurality of computers is operably connected to the video input of the discrete radio frequency switch, wherein the control output of the second logic gate is operably connected to the control of the discrete radio frequency switch.

6. The video switch of claim 1, further comprising [wherein the video output is operably connected to] a peaking operational amplifying [video amplifier] circuit having a video input operably connected to the video output of the discrete radio frequency switch.

7. A video switch for routing video data from a host computer to a target video destination in a KVM system, the video switch comprising:

a plurality of switch circuits configured into a multiplexed circuit, wherein at least one switch circuit of said plurality of switch circuits comprises a discrete radio frequency switch having a control input and a voltage converter operably connected to the control input of the discrete radio frequency switch.

--10. A video switch for switching a selected video output of a plurality of computer video outputs to a target video destination, the video switch comprising:
a plurality of switch video inputs for receiving the plurality of computer video outputs;

at least one switch video output operably connected to the target video destination; and

a plurality of discrete radio frequency switch circuits configured into a multiplexed circuit operably connected to the plurality of switch video inputs and the at least one switch video output for selectively switching the selected video output of the plurality of computer video outputs to the target video destination.--

--11. The video switch of claim 10, each of the plurality of discrete radio frequency switches having a control input, further comprising a plurality of voltage converters corresponding to the plurality of discrete radio frequency switches, each of the plurality of voltage converters operably connected to the control input of a corresponding discrete radio frequency switch of the plurality of discrete radio frequency switches.--

--12. The video switch of claim 10, wherein at least one of the discrete radio frequency switches is a depletion mode MOSFET device.--

--13. The video switch of claim 5 further comprising a peaking operational amplifying circuit having a video input operably connected to the video output of the discrete radio frequency switch.--

--14. A video switch for switching a selected video output of a plurality of computer video outputs to a target video destination comprising:

a plurality of video buses;
a first multiplexer for switching the selected video output of the plurality of computers to a target video bus of the plurality of video buses, the first multiplexer comprising a plurality of first multiplexer video inputs for receiving the plurality of computer video outputs, and a plurality of first multiplexer video outputs operably connected to the plurality of video buses; and
a second multiplexer having a plurality of second multiplexer video inputs operably connected to the plurality of video buses for switching the video signal received from the target video bus to the target video destination, wherein at least one of the first multiplexer and second multiplexer comprises a plurality of discrete radio frequency switches arranged into a multiplexed circuit.--

--15. The video switch of claim 14 further comprising OSD circuitry having an OSD output operably connected to the target video destination and an OSD switch operably connected

to the target video destination for switching off the selected video output while the OSD circuitry is enabled.--

--16. The video switch of claim 15 wherein the OSD switch comprises a discrete radio frequency switch.--

--17. The video switch of claim 1 wherein the video output of the discrete radio frequency switch operably connected to the target video destination, further comprising OSD circuitry having an OSD output operably connected to the target video destination and an OSD switch operably connected to the video output of the discrete radio frequency switch for switching off the video output of the discrete radio frequency switch while the OSD circuitry is enabled.--

--18. The video switch of claim 10 further comprising OSD circuitry having an OSD output operably connected to the target video destination and an OSD switch operably connected to the at least one switch video output for switching off the at least one switch video output while the OSD circuitry is enabled.--

--19. The video switch of claim 10, further comprising a plurality of peaking operational amplifying circuits corresponding to the plurality of discrete radio frequency switches, each of the plurality of discrete radio frequency switches having a video output operably connected to a corresponding peaking operational amplifying circuit of the plurality of peaking operational amplifying circuits.--

--20. A method of displaying an OSD display on a target monitor concurrently with a source video display comprising:

receiving from a first input a source video display signal comprising a source video color component and a positional component;
receiving from a second input an OSD display signal comprising an OSD display color component;

switching off the source color component of the source video display signal when receiving the OSD display signal from the second input; and
synchronizing the source video display signal and the OSD display signal using the positional component of the source video display signal.--

--21. A method of switching a video signal from a source computer to a target video destination comprising:

receiving a control signal of a first voltage for selecting the source computer from one of a plurality of computers;
converting the control signal of the first voltage to a control signal of a second voltage; and

providing the control signal of the second voltage to a multiplexed circuit to switch the video signal from the source computer to the target video destination, wherein the multiplexed circuit comprises a plurality of discrete radio frequency switch circuits.--